

C.V

Dr. MOHAMED ELBABLY

1. Personal Informations

- Date of birth** : *April 5th - 1955*
- Place of birth** : *Egypt*
- Marital status** : *Married, has three children*
- Nationality** : *Egyptian*
- Occupation** : *Assist. Professor in Communications and Electronics Department, Faculty of Engineering at Helwan, Helwan University.*
- Address** : *Communications and Electronics Department, Faculty of Engineering at Helwan, Helwan University-Cairo -Egypt.*
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2. Education and Qualifications

- **Ph.D. degree**, On testability and diagnosability of digital systems, from Brunel University (west London University), U.K, Feb. 1988.
- **M.Sc. degree**, By courses and dissertation Thesis, from UMIST, Manchester University, U.K, Dec. 1984.
- **B.Sc. degree**, Communications and Electronics Dept., May 1978, from Helwan University, with grade “*Excellent with honor rank*”

3. Vocational Experience

- **Administrator** in the communications and Electronics Dept. Faculty of Engineering at Helwan, Helwan University. 1978-1983.
- **Assistant Lecturer** in the communications and Electronics Dept. Faculty of Engineering at Helwan, Helwan University. 1985-1988.
- **Assist. Professor** in the communications and Electronics Dept. Faculty of Engineering at Helwan - Helwan University. 1988 till now, (during this period I spent five years (1991-1996) in Technical College- Riyadh- KSA – also, part time teaching at October 6 university (from 2001 till now)), modern academy for Eng. & Technology at Maadi, MSA university at 6 October city, ERU University at Badr city and CIC at new cairo city.

4. Teaching Experience in the University and consultations

Dr. Mohamed Elbably has a long experience in academic teaching in the university before and after having been awarded his Ph.D. degree. This include:

Undergraduate, postgraduate and training (industry) Courses

1. Electronic circuits (B.sc + M.sc).
2. Design of Logic Circuits (B.sc + M.sc).
3. Computer architecture (M.sc).
4. Computer software languages (C⁺⁺) (B.sc).
5. Testability design techniques of digital circuits (B.sc + M.sc).
6. Fault Diagnosis Techniques of digital systems (at system level) (B.sc + M.sc + training courses in industry).
7. Design of logic micro-controllers (Dipl.)
8. Programmable Logic Controller (PLC) principle and applications (B.sc + Dipl.).

9. Simple, complex programmable Logic Devices (SPLD&CPLD) and Very High speed IC Descriptive Language (VHDL) (B.sc + Dipl. + M.sc + Phd).
10. Advanced Electronics (B.sc + Dipl.)
11. Consultations and supervising of initiating many testing & diagnostic labs. in industrial factories through R&D center at Helwan University.

5. Research Interest

Electronics Design Automation (EDA) for logic controller, testing and diagnostic of digital systems, fault tolerant techniques in digital systems, CPLD (such as MAX), FPGA and VHDL to implement for analogue and digital systems.

6. Publications

- [1] M. Elbably , I. I. Isameel and G. Musgrave “A new design for compression technique for testability design” Military Technical College- Cairo- Egypt, 3rd ASAT Conf., April 1989.
- [2] I. I. Isameel and M. Elbably “Spread spectrum detection using micro-computers” AES’ 89, Alazhar Eng. First Conf., Elect. Eng., Alazhar University- Egypt, Dec. 1989.
- [3] M. Elbably “ Modeling and diagnosability of faulty units in digital computer networks” Military Technical College- Cairo- Egypt, 4th ASAT Conf., May 1991.
- [4] M. Elbably “On self testable design of multiprocessor systems: Testing by the compression approach” Engineering research bulletin,

- Helwan University- Faculty of Engineering – Mataria – Cairo – Egypt, Vol. 2, April 1994.
- [5] M. Elbably “An efficient algorithm and its implementation to control mobile robot” Engineering research journal, Helwan University- Faculty of Engineering – Mataria – Cairo – Egypt, Vol. 4, April 1995.
- [6] M. Elbably “ Adaptive diagnosable algorithm of a fault tolerant multistage interconnection network” Bulletin of the Faculty of Engineering, Assiut University, Vol., 24, part 2, July 1996.
- [7] M. Elbably “ A new diagnostic algorithm to minimize test application time for finite sequential machines” “MICATE’ 99 “ Faculty of Eng., Minia University , March 1999.
- [8] M. Elbably " State machine transition to avoid the race conditions in asynchronous sequential logic circuits” U.R.S.I Proc. Of the 17 th NRSC’ 2000, - Minufiya Univ. Feb. 2000.
- [9] A. Elsayed, M. Elbably and H. Elbolok " An automatic testing technique for PLDs " U.R.S.I Proc. Of the 19 th NRSC’ 2002 – Alex. Univ. March 2002.
- [10] M. Elbably “ New observer analyzer to test CPLDs” Bulletin of the Faculty of Engineering, Assiut University, Vol., 30, part 2, April 2002.
- [11] A. Elsayed, M. Elbably and H. Elbolok "A new approach to test and diagnose the faulty logic blocks"U.R.S.I Proc. Of the 20 th NRSC’ 2003 –part C₂₇ –Cairo - Egypt. March 2003.
- [12] M. Eladawy , M. Elbably, S. Karawya and N. Salem "Automatic detection and measurement of Foveal Avascular zone" U.R.S.I Proc. Of the 20th NRSC’ 2003 – part K₃ – Cairo - Egypt. March 2003.

- [13] M. Elbably " Dynamic diagnosis algorithm to minimize the diagnosis complexity of digital systems" Journal of Engineering and Applied Science - Faculty of Eng. - Cairo University, Vol. 52- No. 4, PP.753-763, Aug. 2005.
- [14] M. Elbably "Adaptive algorithm for routing and placement in FPGA" Journal of Engineering Sciences (JSE) Faculty of Engineering, Assiut University, Vol., 36, No. 6, pp. 1499-1511, . Nov. 2008.
- [15] Ahmed Saeed, M Elbably, G. Abdelfadeel and M. Eladawy "FPGA implementation of Radix- 2² Pipelined FFT Processor " Proceeding of the 8th WSEAS international conf. on Signal Processing, pp. 109-114, May 2009.
- [16] Ahmed Saeed, M Elbably, G. Abdelfadeel and M. Eladawy "Efficient FPGA implementation of FFT/IFFT Processor " International Journal of Circuits, System and Signal processing , Issue 3, Volume 3,pp 103- 11 - 2009.